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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda

Art Unit: 2123

Serial No.: 09/438,321


Examiner: Samarina Makhdoom

Filed: January 14, 2000

Docket: TI-29837

For: EMULATION SYSTEM EMPLOYING SERIAL TEST PORT AND ALTERNATIVE  
DATA TRANSFER PROTOCOL

## AMENDMENT UNDER 37 C.F.R. 1.111

Assistant Commissioner  
for Patents  
Washington, D.C. 20231CERTIFICATION OF FAX TRANSMITTAL  
UNDER 37 C.F.R. §1.6(b)I hereby certify that the above  
correspondence is being facsimile transmitted  
to the Patent and Trademark Office on January  
14, 2003.  
Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated October 18, 2002. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows:

In the Abstract

Please amend the ABSTRACT OF THE DISCLOSURE of this application as follows:

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--Emulation communications via a test access port and boundary-scan architecture providing serial access to a serial connection of a plurality of registers disposed in a plurality of modules. One of the modules is selected for communication. Nonselected modules are made nonresponsive to data on the serial connection. The external emulation hardware supplies a serial signal having a first logic state for a number of cycles greater in number than a number of bits of the serial connection of registers to the test access port. The emulation hardware supplies a start bit having an opposite logic state. The selected module detects the start bit and stores the next predetermined number of data bits. These bits could be data bits to be stored in a program visible data register or bits interpreted as an instruction for execution by the module. The selected module may transmit return communications via the serial scan path using the same format.--